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IV B.TECH – I SEM EXAMINATIONS, NOVEMBER - 2010 EMBEDDED SYSTEMS (COM. IT, CSS)

RR

Time: 3hours

Code.No: **RR411202**

Answer any FIVE questions All questions carry equal marks

1. Discuss various steps involved in the deve example	elopment of an embedded system	with an [16]
2. Explain about various hardware units use	d to build embedded systems.	[16]
3. Explain the architecture and features of 8	051 microcontroller.	[16]
4. Explain the following:		
a) Polling	b) Context switching	
c) Interrupt priority		+4+4+4]
5. Explain the functions of a schedules in a RTOS and how does the scheduler carryout		
those functions.		[16]
6. What is an 'heart beat timer'? Explain wh	iy is it required in RTOS.	[16]
7. Explain the need for encapsulating semap	phores and queues with an example	e. [16]
8. Explain the requirement specifications of	any four Networked Java-Enabled	b
information appliances.		[16]
**	****	

Max.Marks:80

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Time: 3hours	Max.Marks:80		
Answer any FIVE questions All questions carry equal marks			
1. Explain the architecture and features of 8051 microcont	roller. [16]		
2. Explain the following: a) Polling c) Interrupt priorityb) Context s d) Interrupt v	6		
3. Explain the functions of a schedules in a RTOS and how does the scheduler carryout those functions. [16]			
4. What is an 'heart beat timer'? Explain why is it required	l in RTOS. [16]		
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6. Explain the requirement specifications of any four Netw information appliances.	vorked Java-Enabled [16]		
7. Discuss various steps involved in the development of an example	n embedded system with an [16]		
8. Explain about various hardware units used to build emb	edded systems. [16]		

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4. Explain the requirement specifications of information appliances.	any four Networked Java-Enal	bled [16]
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7. Explain the architecture and features of 80	051 microcontroller.	[16]
8. Explain the following:a) Pollingc) Interrupt priority	b) Context switchingd) Interrupt vector table	[4+4+4+4]
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